

Fig. 2A

BWRQ packet (write request for data block packet)

31				0						
destination_ID	rt	tcode	pri							
source_ID										
destination_offset										
data_length	extended_tcode									
header_CRC										
data_field										
zero pad bytes(if necessary)										
data_CRC										
				1 7 .						

transmitted last

Fig. 2B

QWRQ packet

(write request for data quadlet packet)

transmitted first	-			. 0
destination_ID	tl	rt	tcode	pri
source_ID				
destinati	on_offset			_
quadle	t_data	1 1	l ' + _411	
heade	r_CRC			

transmitted last

Fig. 2C

WRS packet

(write response packet)

transmitted first 31				0	
destination_ID	tl	rt	tcode	pri	
source_ID	rcode	reserved			
res	served				
head	der_CRC				

transmitted last

Fig. 2D

BRRQ packet (read request for data block packet)

transmitted first				0						
destination_ID	t1	rt	tcode	pri						
source_ID										
destination_offset										
data_length	ngth extended_tcode									
header_CRC										

transmitted last

Fig. 2E

BRRS packet (read response for data block packet)

transmitted first				0						
destination_ID	t1	rt	tcode	pri						
source_ID		,								
destination_offset										
data_length	extended_tcode									
header_CRC										
data_field										
zero padding(if necessary)										
data_CRC										

transmitted last

Fig. 3A

internal BWRQ packet

spd	tl	rt	tcode	pri						
destination_ID										
destination_offset										
data_length	extended_tcode									
block data										
zero padding(if necessary)										

Fig. 3B

internal QWRQ packet

spd	tl	rt	tcode	pri						
destination_ID										
destination_offset										
quadlet_data										

Fig. 3C

internal WRS packet

				<u> </u>
destination_ID	tl	rt	tcode	pri
source_ID	rcode			
spd				ackSent

Fig. 3D

internal BRRQ packet

spd	tl	rt	tcode	pri						
destination_ID										
destination_offset										
data_length										

Fig. 3E

internal BRRS packet

destination_ID	tl	rt	tcode	pri						
source_ID										
destination_offset										
data_length extended_tcode										
block data										
zero padding(if necessary)										
spd ackSent										

Fig. 4

target 2 (optical disk drive)		ORB_POINTER			analyze	ORB		1	<u>^</u> .	••		↑			^ -
		Transfer ORB's address at initiator (Issue BWRO to ORB POINTER register at target)		Request transmission of the ORB stored at the address	(Issue BRRQ to the ORB's address at initiator) Send the ORB to target	(Issue BRRS with the ORB stored in data field)		Store data at the address specified by the UKB (Issue BWRQ)	(Send ACK representing ack_complete or WRS representing resp_complete)	Store data at the address specified by the ORB	(Issue BWRQ)	(Send ACK representing ack_complete or WRS representing resp_complete)	Send status for the ORB	(Issue BWRQ to status_FIFO specified by the ORB)	(Send WRS representing resp_complete responsive to the BWRQ)
initiator 1 (host PC)	create ORB	(E)	→	`	$(2) \begin{cases} \\ \\ \end{cases}$	ا ب	(<u> </u>	(6)	(S) ↑				(4)	<u>.</u>

-inform reception of packet addressed to CPU BWRQ #3 → time Ack Comp BRRQ detection WRS WRS Ack Pend wait for response BWRQ #3 data #3 BWRQ #3 /BRRQ1 BRRQ suspension -Ack Pend data #3 BWRQ #2 header #3 Ack Comp response detection WRS WRS wait for response data #2 BWRQ #2 BWRQ #2 data #2 Ack Pend header #2 BWRQ #1 ∠ boot data exchange data #1 data #1 BWRQ #1 BWRQ #1 header #1 က DMAC 13 10 ∞ 6 1394 bus _ П വ 9 RFIL DTRF PPRC TFIL ATF ARF CPU

Fig. 6A







